

ABSTRACT OF DISCLOSURE

A plasma display panel including a first panel, address electrodes formed on the first panel in a predetermined pattern, a first dielectric layer formed on the first panel and covering the address electrodes, a partition structure having unit partitions discontinuously formed on the first dielectric layer to partition a discharge space, the unit partitions being parallel to the address electrodes and each having auxiliary partitions, red, green and blue phosphor layers coated in the partitioned discharge space, a second panel, which is coupled to the first panel to form the discharge space and which is transparent, a plurality of pairs of sustaining electrodes formed on an inner surface of the second panel and having sets of first and second electrodes at a predetermined angle with respect to the address electrodes, and a second dielectric layer formed on the second panel and covering the sustaining electrodes.